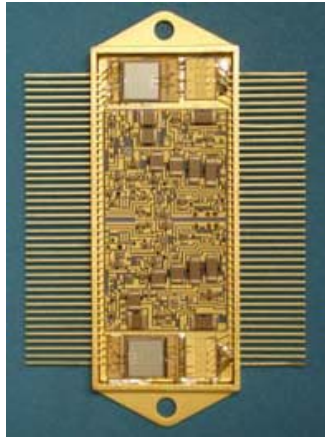


Hybrid Microelectronics

<http://www.cmcelectronics.ca/En/Prodserv/Hybrid-Microelectronics.html>



Design and assembly

- Analog: DC to GHz's
- Digital: Gates, Memory, Microcontroller
- Power:



Cost effective

- Automated Pick & Place
- Automated Wire Bonding
- ATE (Test)
- Thick Film Line



Hi Power Laser & VCSEL

- Single and array
- Up to 100W laser
- Up to 60W VCSEL
- Multijunction 905 & 1550nm
 - 2.8W/A 905nm
 - 1 W/A 1550nm

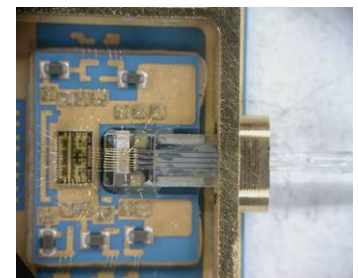
APD + TIA Receiver

- Si or InGaAs low k
- 100W recovery to nw's <2us
- TPG with Digital output



Fiber modules

- Hermetic
- External passive alignment
- 175°C fibers for STD Assy Processes



- **Complete ESS Capability**
- **Gun Hardened Designs > 10kg's**
- **In House Qual**
- **In House CCA (Circuit Card Assy Line)**

Montreal Facility

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