

Test seminar provided by Averna, JTAG technologies and Northtouch Canada

Join high-tech leaders **Averna**, **JTAG Technologies**, **Northtouch Canada**, **National Instruments**, **Teradyne** and **Acculogic** for a full day of enlightening technical presentations, discussions and networking opportunities.

Take your PCB testing to a higher level!

WHEN: Thursday, October 27, 8:00 am – 5:00 pm

WHERE: [Holiday Inn Select, Kanata, Ontario, K2T 1E6](#)

COST: Free!

Register

SEMINAR SCHEDULE

8:00 – 9:00 am

Breakfast

9:00 – 10:15

DFT and Guidelines for Boundary Scan, Rick Pierpont, JTAG Technologies

10:30 – 11:30

Introduction to NI TestStand: The Industry Standard Test Management Software, Tejinder Gill, National Instruments

12:00 – 12:45

Lunch, provided by JTAG and Averna

1:00 – 2:30

The Evolution of ICT, Alan Albee, Teradyne

2:45 – 3:45

The Flying Probe Approach to Limited Access Testing and Basic DFT, Greg LeBlonc, Acculogic

4:00 – 5:00 pm

Functional Test Best Practices, Gabriel Maréchal, Averna

HOTEL RECOMMENDATION

-Holiday Inn Select, Kanata

-Country Inn & Suites by Carlson, Ottawa West

-Best Western, Nepean

Click here to access the Seminar Registration Page for more information :

www.averna.com/go/averna-jtag

Presenters :

